

FIGURE 1
PRIOR ART

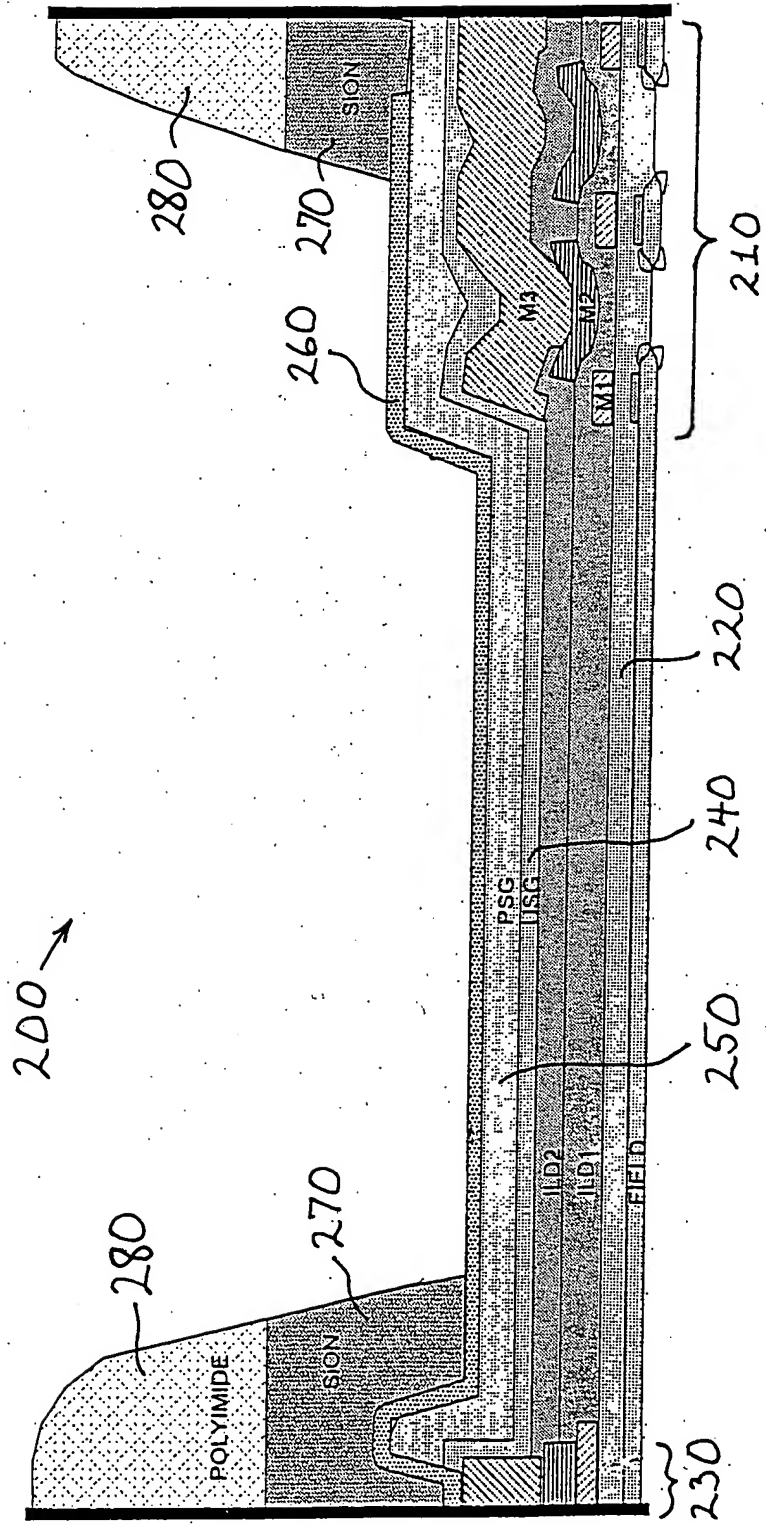


FIGURE 2

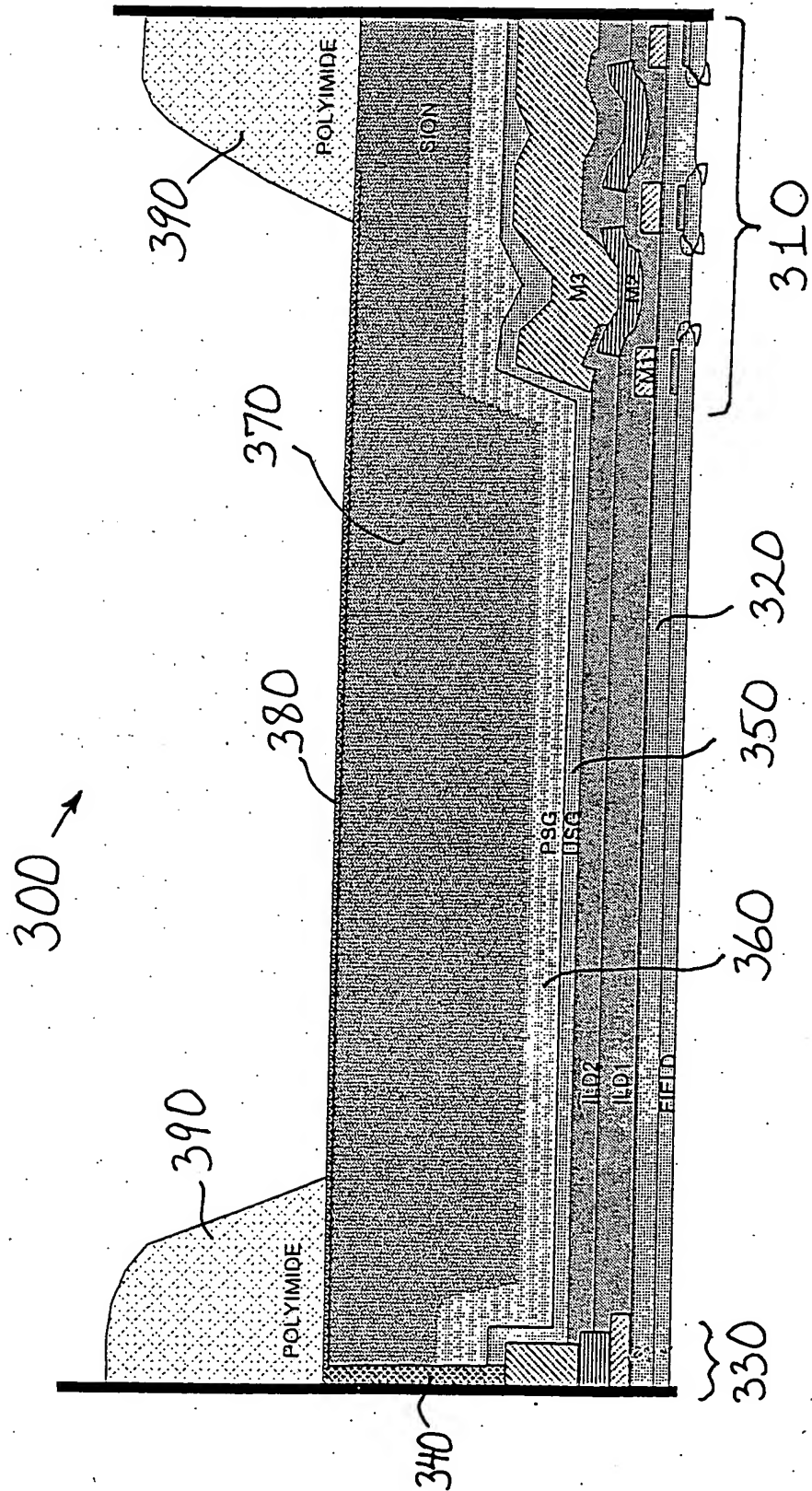


FIGURE 3

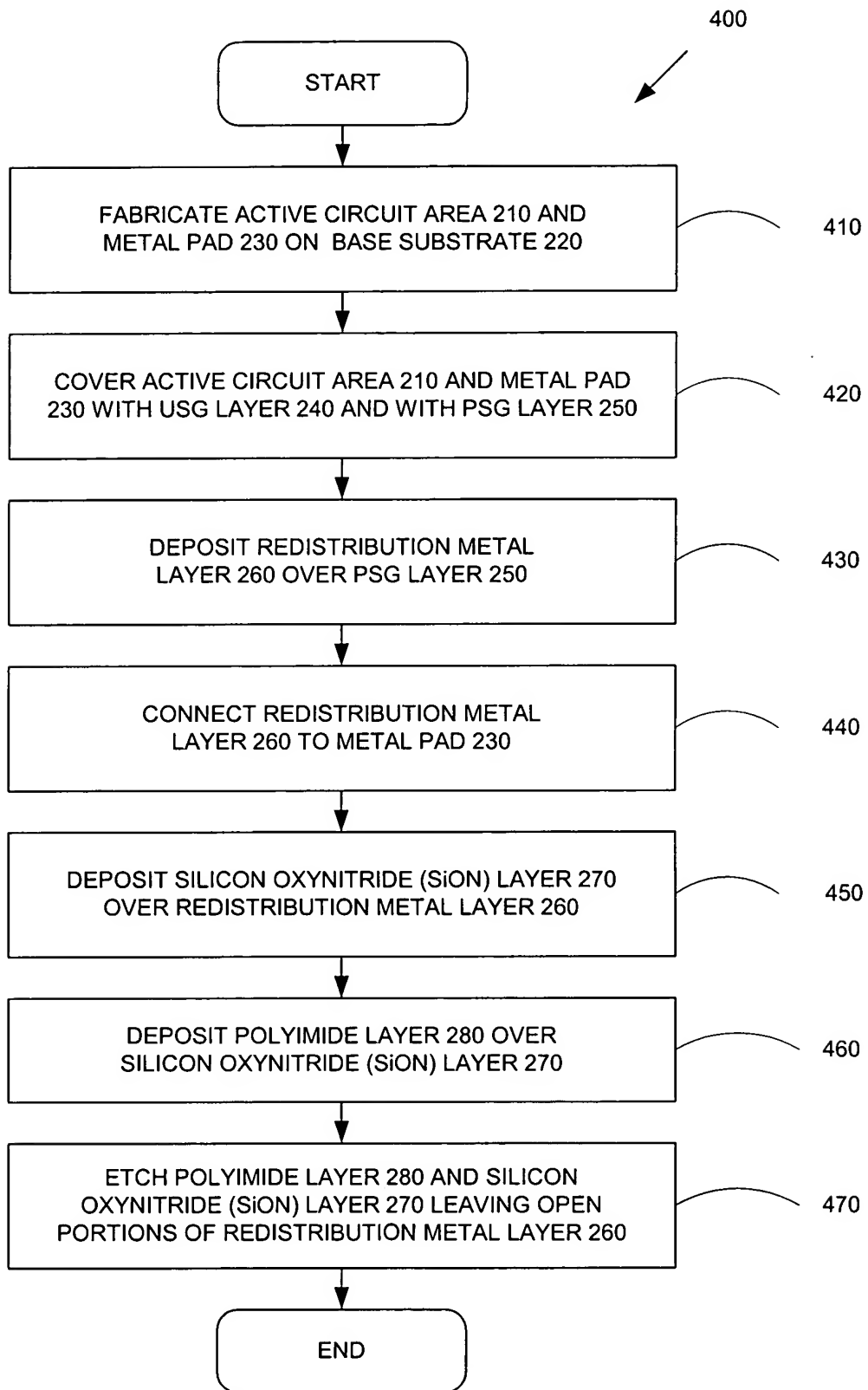


FIGURE 4

FIG. 5 is a cross-sectional view of a semiconductor device 500, showing a substrate 510, a gate stack 520, a gate electrode 530, a gate insulator 540, a gate spacer 550, a gate contact 560, and a gate terminal 570.

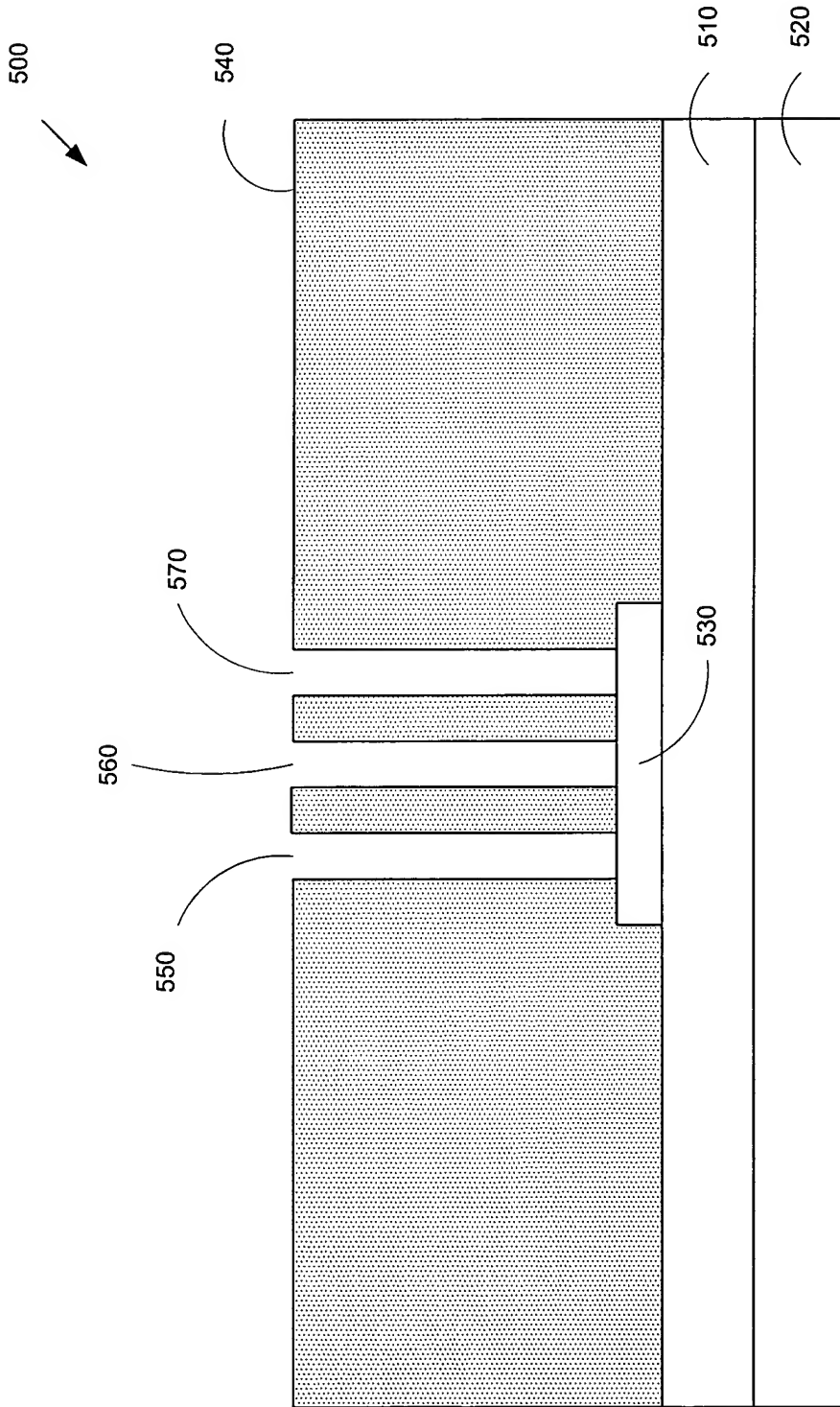


FIGURE 5

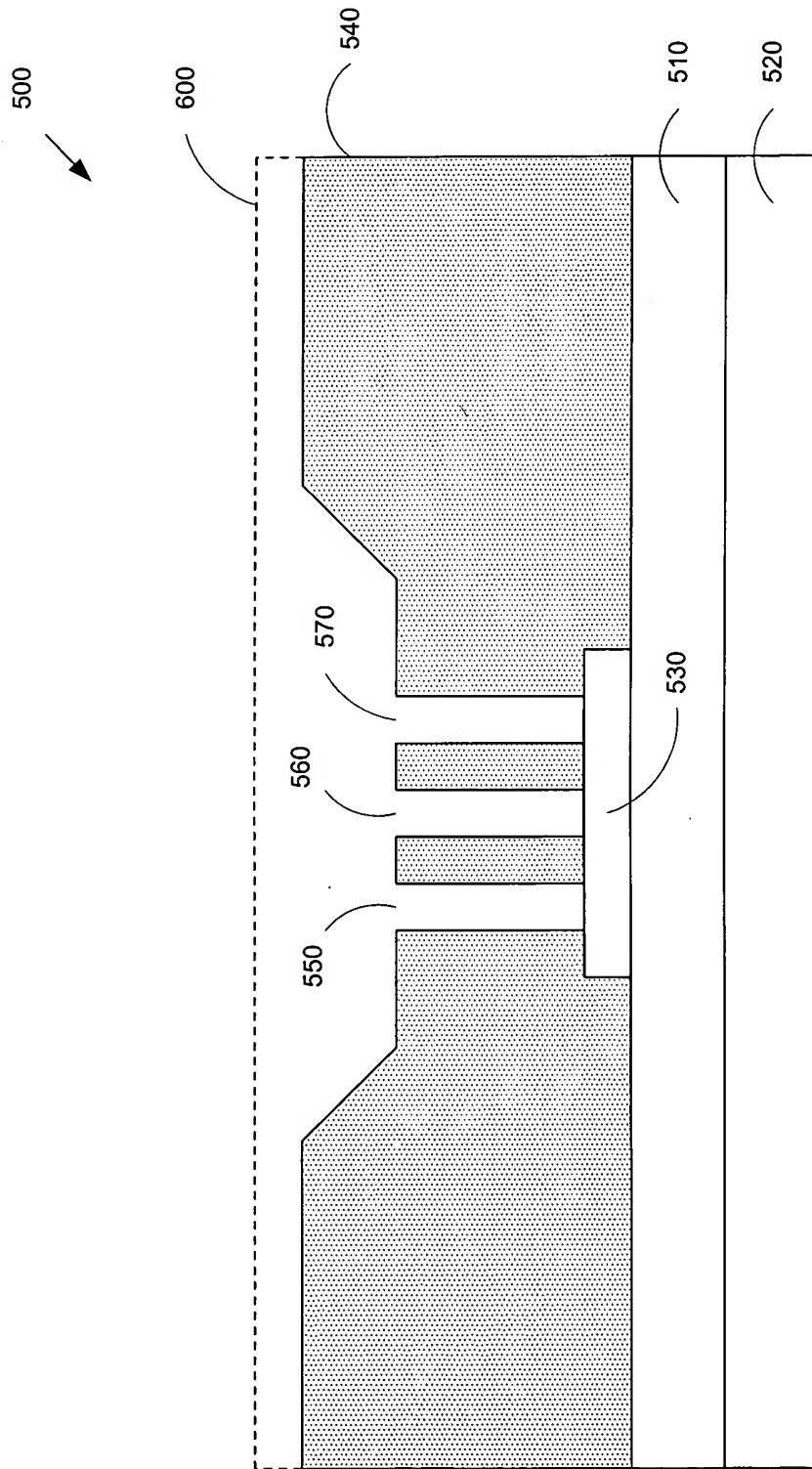


FIGURE 6

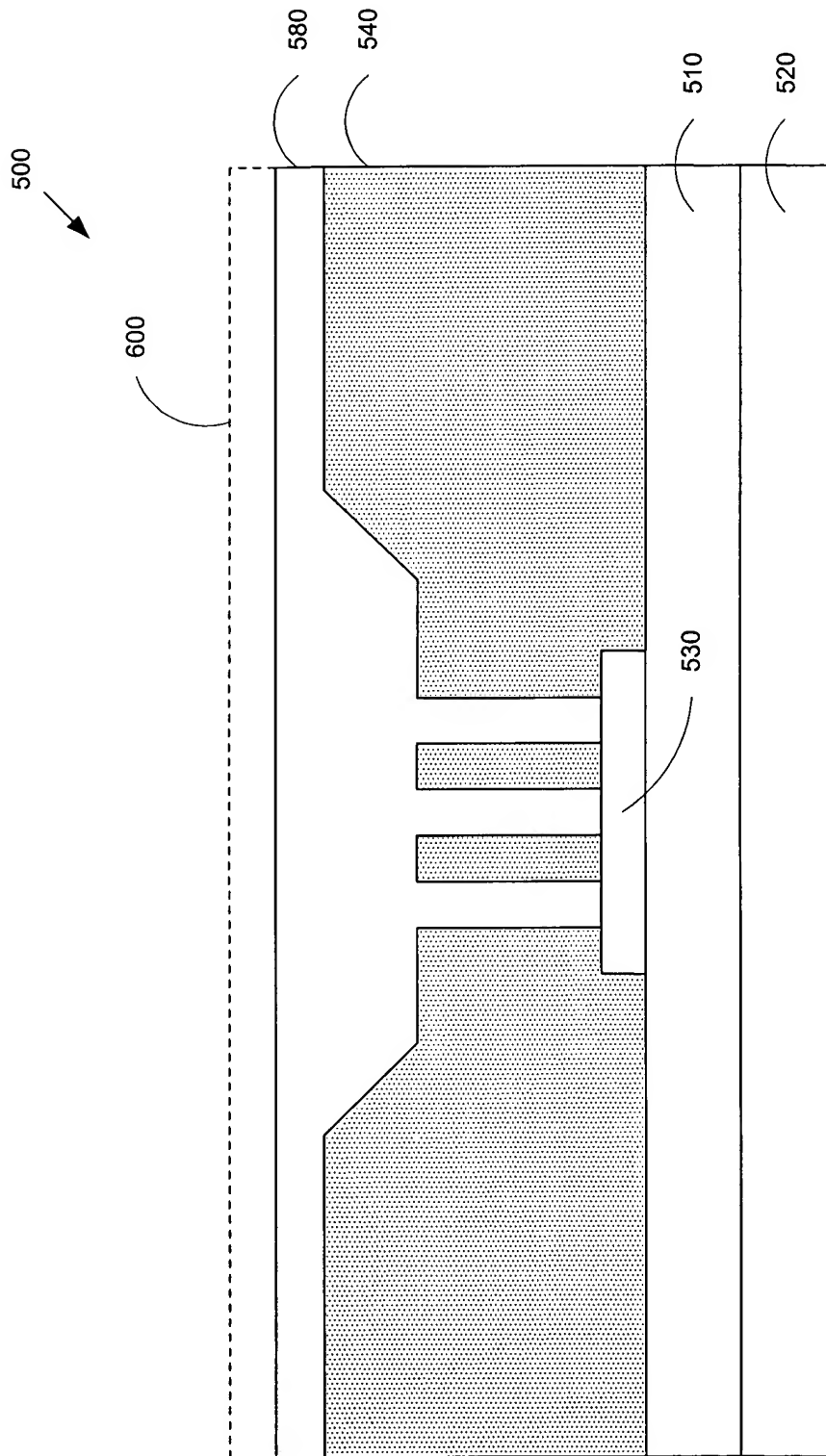


FIGURE 7

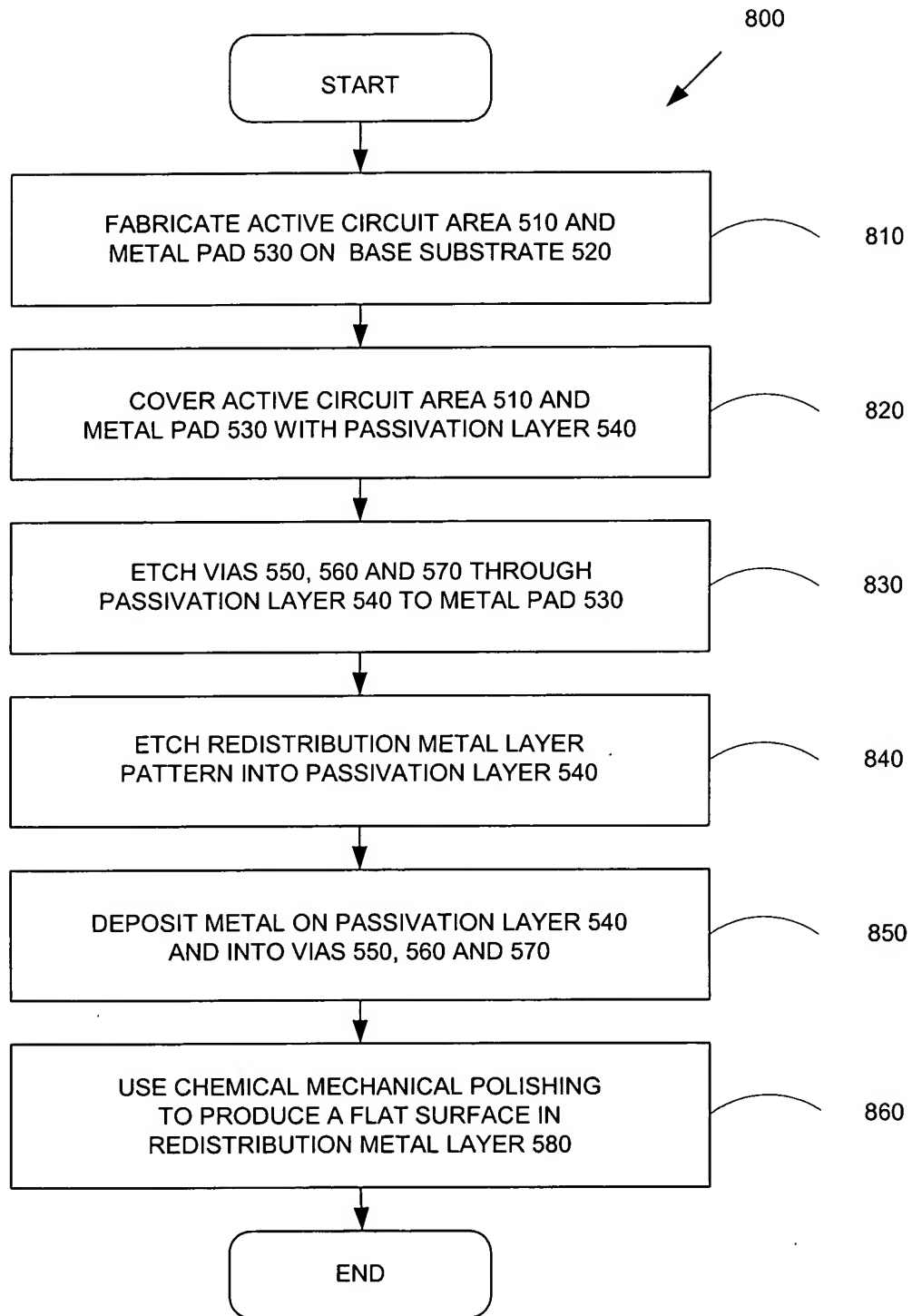


FIGURE 8

900

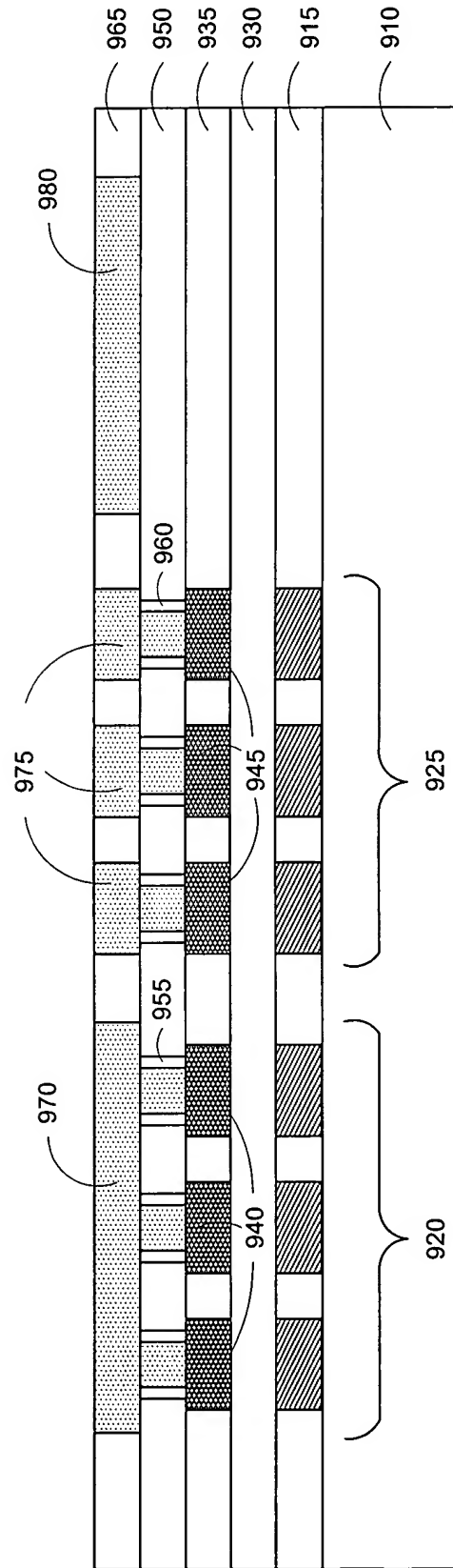


FIGURE 9